Models 708+ & 788+ DC Strain Gage Conditioners Model 728+ Mechanical/Fluid Power Instrument



Model 728+ - Power Instrument for a directly wired strain gage load sensor, and a frequency output transducer.



Model 788+ 2 channel dc strain gage signal conditioner - for directly wired torque, flow, pressure, and head transducers.

*NIST Traceable mV/V calibration performed in our

NVLAP accredited laboratory (Lab Code 200487-0)

- Model 728+ reads, displays, processes and outputs
 - shaft torque, speed, power
 - pump/motor head, flow, fluid power
 - drawbar force, velocity, power
- All Models have shunt, load and 0.01% accurate mV/V calibration*
 - 7,800 samples/sec. for strain gage channels
 - 1 millisecond response for speed, flow or velocity input
 - 6 full digit engineering unit display with user definable legends
 - RS232, RS422 or RS485 serial communication
 - auto-scaled selectable Voltage or Current analog outputs
 - assignable logic I/O's, fast max/min capture, real time calculations
 - protected excitation supplies, no pots, no batteries, no fans, no maintenance

These advanced instruments provide engineering unit display of one (Models 708+ and 728+) or two (Model 788+) directly wired strain gage transducers, and one frequency input (Model 728+). What's more, 20 of the most useful processing functions plus real-time calculations are built in. Those capabilities, and the embedded State Machine, make these Instruments powerful testers easily configured to handle complex, Event Driven Testing.

The alphanumeric readout can display measured and computed data, units of measure and test status. During setup, it guides you with English language prompts. There are <u>no manual adjustments</u>. To calibrate, enter the full scale value in engineering units and the instrument provides 0.001% resolution and voltage or current analog outputs (user selectable). The keyboard accesses measured data, held data, max/min data, data spread, limit status, and/or I/O status <u>without test disruption</u>. Password protection may be used, if needed.

The strain gage mV/V calibrator has NIST* Traceable 0.01% absolute accuracy. Manual adjustments aren't needed. Constant delay, Bessel filters are used to suppress noise without introducing overshoots or delay distortion. The frequency conditioner uses a proprietary algorithm to achieve fast response and 0.01% resolution at any full scale. With a quadrature input, it outputs magnitude and direction.

Excitation is furnished for both sensors; <u>no need for external power</u>. Models 708+, 788+ and 728+ are <u>easy to use</u>, <u>have high signal-to-noise ratio</u>, <u>high line frequency common mode rejection</u>, <u>offer great stability and require no maintenance</u> – there are no batteries, fans or pots.

Select either RS232, RS422, or RS485 communications to remotely acquire data, and setup and control the instrument modes. Input actions and output events can be controlled by user configurable logic I/O's. When used in its State Mode, Event Driven Tests can be done - without special hardware or software. That is, the Instrument setup automatically changes as the test moves between states; up to 8 states are possible - see AN7000 for details.

The unprecedented absolute accuracy of the mV/V calibration function eliminates any need for a mV/V transfer standard. Applications for these flexible Instruments run the gamut from an accurate, stable transducer readout to a sophisticated tester capable of complex, event driven testing. No other device offers such power and versatility.

Included software remotely controls all instrument functions from a Windows based PC. It displays, plots and saves realtime data, does X-Y plots, and will also save and download the instruments setup parameters.

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Specifications for Models 708 + /788 + /728 + Instruments

Strain Gage Input(s) (directly wired, not trans	
Connections	
Transducer Excitation	
Automatic Zero Range	
Tare Range (Tare may be actuated from Auto Calibration	keypad or remotely via logic I/O or serial communication port) $\dots \dots \dots \pm 100\%$ of Full Scale
mV/V Type	Dual polarity calibration with provision for CAL resistor feedback. ±1mV/V to ±4.5mV/V. Absolute accuracy is ±0.01% typical, 0.02% worst case. 130dB for 60Hz common mode signal. 200Hz, 5 pole Bessel response filter.
Low Pass Filtering	
	 4.5mV/V F.S. and 5V Excitation: 86/74/68/65dB with 1/10/100/200Hz filters. 4.5mV/V F.S. and 10V Excitation: 86/86/74/68dB with 1/10/100/200Hz filters.
	ne uni-directional or bi-directional (quadrature) source including self generating and zero velocity speed pickups
	flowmeters, etc. When used with bi-directional sensors, the conditioner outputs both direction and magnitude
	Differential or single ended inputs. $100k\Omega$ differential, $50k\Omega$ single ended
Input Signal Bandwidth	
Low Pass Filter (keypad selectable)	
Overall Accuracy	±0.01% of F.S
	+12V@125mA or +5V@250mA short circuit (current limit) and overvoltage (fuses) protected
	2 line by 16 alphanumeric characters, each 0.2" wide by 0.3" high. Backlit LCD with adjustable contrast
Views	
Data Format	
	ngineering units with 6 digits (1-2-5 format) and 5 character, upper/lower case, user-entered legend/descriptor
System Response (per channel)	ngineering units with 6 digits (1-2-5 format) and 5 character, upper/lower case, user-entered legend/descriptor
System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate	7,800Hz (hardware channels and CH3 calculation)
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System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256µs (hardware channels and CH3 calculation) 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare,
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System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type External + 5VDC Power (on I/O connector) Serial Communication Port (selectable as RS2 BAUD Rate 120\(\Omega \text{Termination Resistors (RS485)} \) RS422/485 Transceivers RS232 Drivers Serial I/O's	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256μs (hardware channels and CH3 calculation) 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare, Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT HI Limit, IN Limit, LO Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min. Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals. User enabled/disabled. Permits up to eight states and allows event driven testing. See AN7000 for details are channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or without hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes. Each with programmable destination, protected to ±130VDC or 130Vrms. TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ 0V. Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected. Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected 250mA, short circuit (current limit) and overvoltage (fuse) protected 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232). User selectable for RXD and TXD. Slew-rate limited, short circuit protected (current & thermal limits)
System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type External + 5VDC Power (on I/O connector) Serial Communication Port (selectable as RS2 BAUD Rate 120Ω Termination Resistors (RS485) RS422/485 Transceivers RS232 Drivers Serial I/O's Commands Non-Volatile Memory Storage for System Sette	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256μs (hardware channels and CH3 calculation) 3,900Hz 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare, Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT HI Limit, IN Limit, NOT IN Limit, LO Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min. Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals User enabled/disabled. Permits up to eight states and allows event driven testing. See AN7000 for details Each channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or withour hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes Each with programmable destination, protected to ±130VDC or 130Vrms TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ 0V Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected 12, RS485. Supports 32 devices on RS485 port and 1 device on RS232/422) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) User selectable for RXD and TXD Slew-rate limited, short circuit protected (current & thermal limits) Short circuit protected (current limit) Use a 9 pin D connector. They are ±15kV ESD protected and float (100kΩ) with respect to Earth Ground Control of all modes, settings, and measurements
System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type External + 5VDC Power (on I/O connector) Serial Communication Port (selectable as RS2 BAUD Rate 120Ω Termination Resistors (RS485) RS422/485 Transceivers RS232 Drivers Serial I/O's Commands Non-Volatile Memory Storage for System Sett Dual Analog Outputs	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256μs (hardware channels and CH3 calculation) 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare, Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT II Limit, IN Limit, NOT IIN Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals User enabled/disabled. Permits up to eight states and allows event driven testing. See AN7000 for details Each channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or without hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes Each with programmable destination, protected to ±130VDC or 130Vrms TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ OV Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected (surent limit). Slew-rate limited, short circuit protected (current & thermal limits). Slew-rate limited, short circuit protected (current limit). Use a 9 pin D connector. They are ±15kV ESD protected and float (100kΩ) with respect to Earth Ground Control of all modes, settings, and measurements. EEPROM, batteries are not used Each assignable to any of the 3 channels are short circuit (current limit) and overvoltage (fuse) protected
System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type External +5VDC Power (on I/O connector) Serial Communication Port (selectable as RS2 BAUD Rate 120\Omega Termination Resistors (RS485) RS422/485 Transceivers RS232 Drivers Serial I/O's Commands Non-Volatile Memory Storage for System Sett Dual Analog Outputs Output Impedance/Minimum Load Resis	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256μs (hardware channels and CH3 calculation) 3,900Hz 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare, Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT HI Limit, IN Limit, NOT IN Limit, LO Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min. Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals User enabled/disabled. Permits up to eight states and allows event driven testing. See AN7000 for details Each channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or withour hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes Each with programmable destination, protected to ±130VDC or 130Vrms TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ 0V Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected 12, RS485. Supports 32 devices on RS485 port and 1 device on RS232/422) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) User selectable for RXD and TXD Slew-rate limited, short circuit protected (current & thermal limits) Short circuit protected (current limit) Use a 9 pin D connector. They are ±15kV ESD protected and float (100kΩ) with respect to Earth Ground Control of all modes, settings, and measurements
System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type Six Logic Outputs Type Serial Communication Port (selectable as RS2 BAUD Rate 120Ω Termination Resistors (RS485) RS422/485 Transceivers RS232 Drivers Serial I/O's Commands Non-Volatile Memory Storage for System Sett Dual Analog Outputs Output Impedance/Minimum Load Resis Full Scale ³ Overrange	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256µs (hardware channels and CH3 calculation) 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT HI Limit, IN Limit, NOT IN Limit, LO Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals. User enabled/disabled. Permits up to eight states and allows event driven testing. See AN7000 for details Each channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or without hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes Each with programmable destination, protected to ± 130VDC or 130Vrms TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ 0V Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected. 32, RS422, or RS485. Supports 32 devices on RS485 port and 1 device on RS232/422) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) User selectable for RXD and TXD Slew-rate limited, short circuit protected (current & thermal limits) Slew-rate limited, short circuit protected (current limit). Use a 9 pin D connector. They are ± 15kV ESD protected and float (100kΩ) with respect to Earth Ground Control of all modes, settings, and measurements. EEPROM, batteries are not
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System Response (per channel) Data Sampling & Max/Min Update Rates Limit Checking Rate Logic I/O Response Time Update Rate for Each Analog Output System Control Input Actions/Channel Output Events/Channel HI Li Eight User-defined Patterns State Machine Capability Limit Checking Four Logic Inputs Type Six Logic Outputs Type External +5VDC Power (on I/O connector) Serial Communication Port (selectable as RS2 BAUD Rate 120\Omega Termination Resistors (RS485) RS422/485 Transceivers RS232 Drivers Serial I/O's Commands Non-Volatile Memory Storage for System Sett Dual Analog Outputs Output Impedance/Minimum Load Resis Full Scale ³ Overrange Non-linearity Protection Size and Weight Operating Temperature	7,800Hz (hardware channels and CH3 calculation) 3,900Hz (hardware channels and CH3 calculation) 256μs (hardware channels and CH3 calculation) 3,900Hz All I/O functions can be OR'd in any combination. The pattern function adds ANDing capabilities Logic inputs, outputs, and internal Matrix signals control following actions. Tare, Clear Tare, Hold, Clear Hold, Reset Max/Min, Clear Latched Limits, Check Limits, Do Max/Mins, Apply +CAL, Apply -CAL The following events drive Logic outputs and internal Matrix signals mit, NOT HI Limit, IN Limit, NOT IN Limit, LO Limit, NOT LO Limit, At Max, NOT At Max, At Min, NOT At Min Patterns of Logic inputs, outputs and Matrix signals drive Logic outputs and internal Matrix signals Each channel has a HI and LO limit which may be latched or unlatched, absolute or signed, and with or without hysteresis. Select either Current, Max, Min, Spread or Held data for limit checking Limit violations on any or all channels can be set to trigger backlight flashing in any of the display view modes Each with programmable destination, protected to ±130VDC or 130Vrms TTL compatible, Schmitt Trigger, low-true with 2kΩ pull-up. Input current is -1.5mA @ OV Each with programmable source, short circuit (current and thermal limits) and overvoltage (fuse) protected Open collector, low-true. Operating @ 24V (max) and 0.3A max sink current 250mA, short circuit (current limit) and overvoltage (fuse) protected 32, RS422, or RS485. Supports 32 devices on RS485 port and 1 device on RS323/242) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft (RS232) 1.2to 230.4kBaud. Maximum Cable Length: 4000ft (RS422/RS485), 50ft
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- The ratio expressed in decibels (dB), of full scale (F.S.) signal to noise spread. Measurements are made for a 1 minute interval using a 100Ω source impedance. Notes:
 - Both excitation voltages can be used simultaneously with the following restrictions: $4.8 \times (12 \text{V current}) + (5 \text{V current}) \leq 700 \text{mA}$. AND 12V current $\leq 125 \text{mA}$ AND 5V current $\leq 250 \text{mA}$. Specification is subject to change without notice. 2.